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PTO/SB/30 (10/2001)  
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**REQUEST  
FOR  
CONTINUED EXAMINATION (RCE)  
TRANSMITTAL**

Address to:  
Commissioner for Patents  
Box RCE  
Washington, DC 20231

Application Number	10/006,334
Filing Date	December 3, 2001
First Named Inventor	KAMESH V. GADEPALLY
Art Unit	2818 #10 RCE
Examiner Name	QUOC DINH HOANG M. Braun
Attorney Docket Number	NSC1-G0610 [P04402 P01] 4/10/03

This is a Request for Continued Examination (RCE) under 37 CFR 1.114 of the above-identified application  
Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8,

1. **Submission required under 37 CFR §1.114**

- a. ☐ Previously submitted
- i. ☐ Consider the amendment(s)/reply under 37 CFR §1.116 previously filed on \_\_\_\_\_  
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on \_\_\_\_\_
- iii. ☐ Other \_\_\_\_\_
- b. ☒ Enclosed
- i. ☒ Amendment/Reply      iii. ☐ Information Disclosure Statement (IDS)
- ii. ☐ Affidavit(s)/Declaration(s)      iv. ☐ Other \_\_\_\_\_

2. **Miscellaneous**

- a. ☐ Suspension of action on the above-identified application is requested under 37 CFR §1.103(c) for a period of \_\_\_\_\_ months (Period of suspension shall not exceed 3 months; Fee under 37 CFR §1.17(i) required)
- b. ☐ Other \_\_\_\_\_

3. **Fees** The RCE fee under 37 CFR §1.17(e) is required by 37 CFR §1.114 when the RCE is filed.

- a. ☐ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 50-1697
- i. ☒ RCE fee required under 37 CFR §1.17(e)
- ii. ☐ Extension of time fee (37 CFR §§1.136 and 1.17)
- iii. ☐ Other \_\_\_\_\_
- b. ☒ Check in the amount of \$ 750.00 enclosed
- c. ☐ Payment by credit card (Form PTO-2038 enclosed)

**WARNING:** Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED**

Name (Print / Type)	ALFRED A. EQUITZ	Registration No. (Attorney / Agent)	30,922
Signature	<i>Alfred A. Equitz</i>	Date	April 3, 2003

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner For Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent and Trademark

Name (Print / Type)	Marsha A. Townsend	Date	April 3, 2003
Signature	<i>Marsha A. Townsend</i>		

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KAMESH V. GADEPALLY

Application No. 10/006,334

Filed: December 3, 2001

For: **METHOD FOR MANUFACTURING  
AN INTEGRATED CIRCUIT  
STRUCTURE WITH LIMITED  
SOURCE SALICIDATION**

Group Art Unit: 2818

Examiner: QUOC DINH HOANG

**RESPONSE TO FINAL  
OFFICE ACTION**

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San Francisco, CA 94104  
(415) 433-2250

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GIRARD & EQUITZ LLP

Date: 04/03/03

By:

*Marsha A. Townsend*  
Marsha Ann Townsend

Box RCE  
Commissioner for Patents  
Washington, DC 20231

Sir:

This is in response to the final Office Action mailed on February 5, 2003, in the referenced application and is being filed with a Request for Continued Examination of the application. Please amend the application and consider the following remarks.

IN THE SPECIFICATION:

Amend lines 10-13 on page 7 to read as follows (the attached appendix sets forth the amended section of the specification with markings showing the differences between the original text and the text as hereby amended):

31 accordance with the present invention have been performed. Silicon gates 120 and 122 (typical thickness being in the range between 1500 and 4000 angstroms) overlie the gate oxide layer 110 of MOS transistor structures 104 and 106, respectively. Silicon gates 120 and 122 can be made from either